ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

SYSTEM AND METHOD OF AUTOMATICALLY GENERATING KERF DESIGN DATA

Application Number:

10/605,109

Confirmation Number:

Howard Barrett

First Named Applicant:
Attorney Docket Number:

BUR920030106US1

Art Unit:

2/2/

Examiner:

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Search string:

(6553559 or 6330708 or 20030009739).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
1740	1	6553559	2003-04-22	Liebman et al.		716	19
MY	2	6330708	2001-12-11	Parker et al.		716	19

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init Cite.N	lo Pub. No.	Date	Applicant	Kind	Class	Subclass	
M7) 1	20030009739	2003-01-09	Watanabe et al.		716	19	
Signatur	e And	11/30/05					
	Examiner-Na	Date					
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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT BUR920030106US1 GAPPLICANT: Howard T. Barrett, et al.										
(Use several she	eets if necessary)	PETER	FILING DATE: 09/09	FILING DATE: 09/09/03			GROUP: 2821 Unassigned			
REFERENCE DESIGNATION U.S. PATENT DOCUMENTS										
EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLA	SS	SUBCLASS		G DATE PPRO.)		
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FOREIGN PATENT DOCUMENTS										
	DOCUMENT NUMBER	DATE	COUNTRY	CLAS	s su	ЉCLASS	TRANSI YES	ATION NO		
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OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)										
K. Ahdoot, et al., "IBM FSD VLSI Chip Design Methodology", Proceedings of Twentieth Design Automation Conference on Design Automation, June 1983, pp. 39-45.										
						$\overline{}$				
EXAMINER DATE CONSIDERED										
			t citation is in conformance wit	h MPEP	609.	Draw line	through	citation if		